FIG. 1A

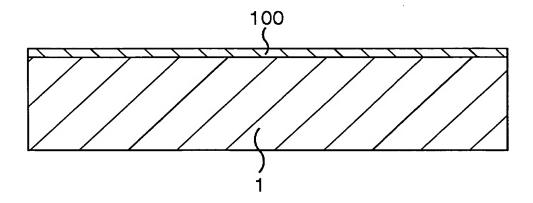


FIG. 1B

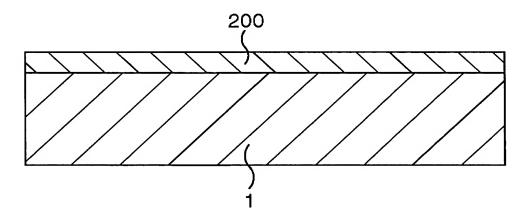


FIG. 2A

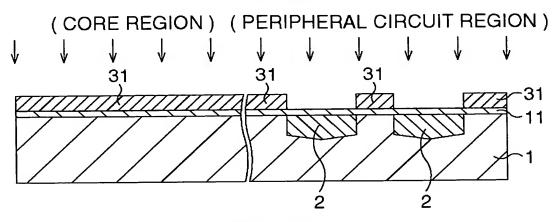


FIG. 2B

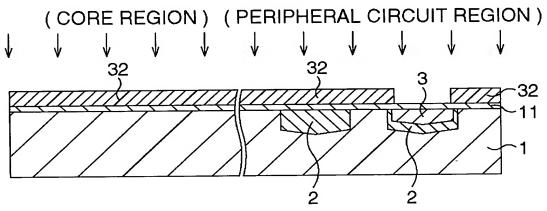


FIG. 2C

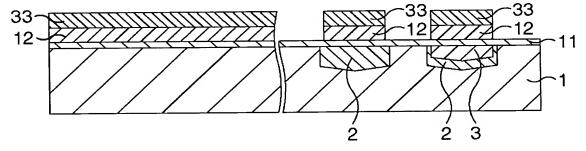


FIG. 2D

(CORE REGION) (PERIPHERAL CIRCUIT REGION)

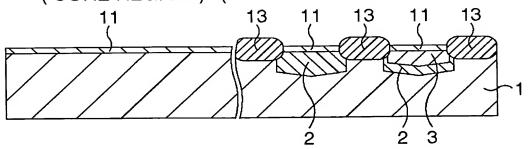


FIG. 3A

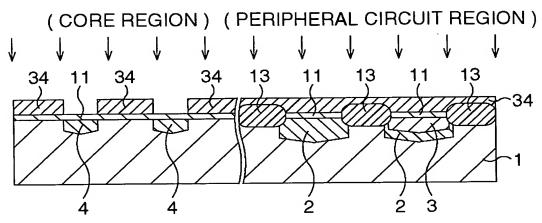


FIG. 3B

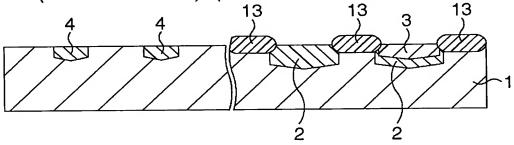


FIG. 3C

(CORE REGION) (PERIPHERAL CIRCUIT REGION)
14 13 14 13 14 13

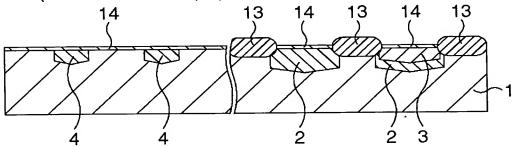


FIG. 3D

(CORE REGION) (PERIPHERAL CIRCUIT REGION)

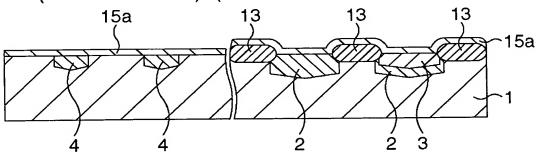


FIG. 4A

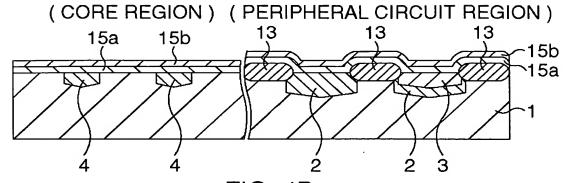


FIG. 4B

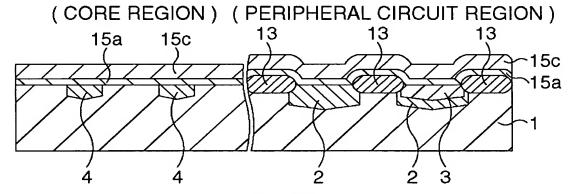


FIG. 4C

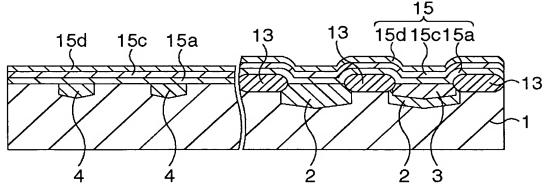


FIG. 4D

(CORE REGION) 15d 15c 15a 35 (PERIPHERAL CIRCUIT REGION)

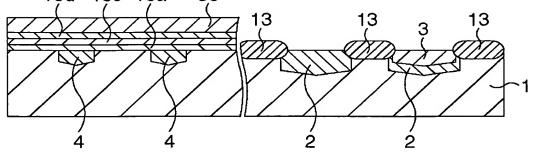


FIG. 5A

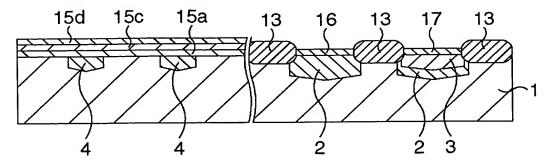


FIG. 5B

(CORE REGION) (PERIPHERAL CIRCUIT REGION)

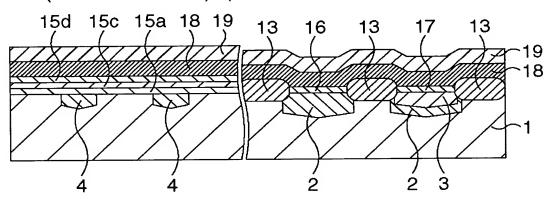


FIG. 5C

(CORE REGION) (PERIPHERAL CIRCUIT REGION)

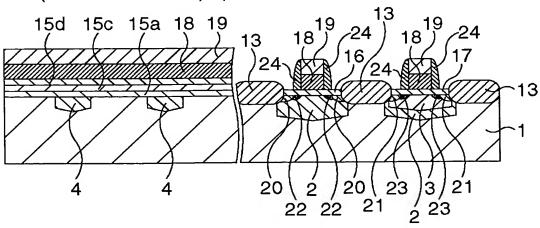


FIG. 6A

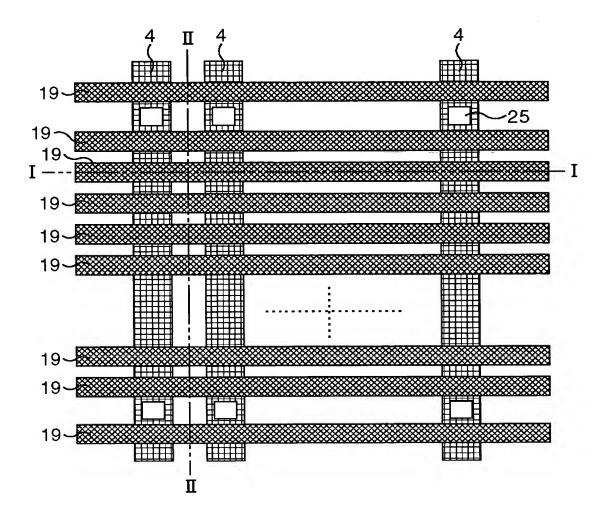
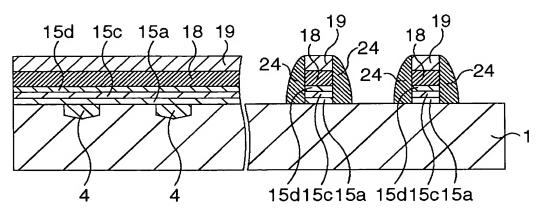


FIG. 6B

(I-ICROSSSECTION) (II-IICROSSSECTION)



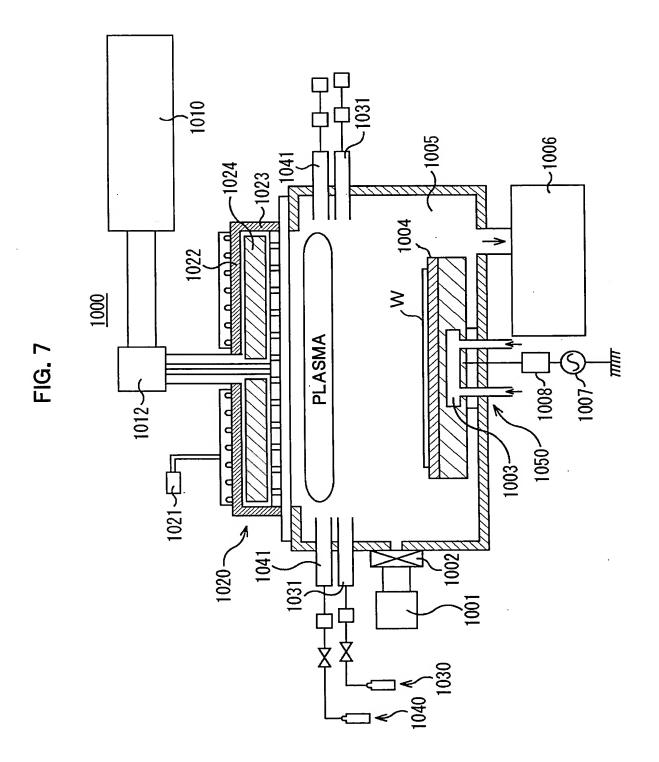


FIG. 8A

